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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Youngmin Kim, et al.

Docket No: TI-29012

Serial No: 09/574,653

Conf. No: 8503

Examiner: Hsien Ming Lee

Art Unit: 2823

Filed: 05/18/2000

For: A TUNABLE SIDEWALL SPACER PROCESS FOR CMOS INTEGRATED CIRCUITS

REQUEST FOR CORRECTED FILING RECEIPT

Assistant Commissioner for Patents
Office of Initial Patent Examination
Customer Service Center
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being
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20231 on 2-14-03

Ann Trent
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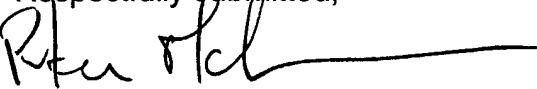
Sir:

Enclosed is a copy of the Filing Receipt for United States Patent Application Serial Number 09/574,653.

Please note the correction marked in red. Please correct inventor Youngmin Kim's address as shown, replacing "KOREA, REPUBLIC OF" with "TX". Enclosed is a copy of the Declaration and Power of Attorney that was filed with the application on May 18, 2000.

Applicants respectfully request a corrected Filing Receipt and believe that Applicants did not cause this error and that no fee is due. However, this letter authorizes any necessary charges to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



Peter K. McLarty
Attorney for Applicants
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Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
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CORRECTED FILING RECEIPT



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/574,653	05/18/2000	2811	768	TI-29012	2	19	4

Peter K McLarty
 Texas Instruments Incorporated
 P O Box 655474 M/S 3999
 Dallas, TX 75265

Date Mailed: 08/24/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Youngmin Kim, Allen, TX
 Shawn T. Walsh, Richardson, TX ;

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Continuing Data as Claimed by Applicant

THIS APPLN CLAIMS BENEFIT OF 60/137,774 06/04/1999

Foreign Applications

If Required, Foreign Filing License Granted 07/19/2000

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BEN KROGER

Title

A tunable sidewall spacer process for cmos integrated circuits

Preliminary Class

257

Data entry by : SHEFFEY, CATHERINE

Team : OIPE

Date: 08/24/2000



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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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- If your application was submitted under 37 CFR 1.10, your filing date should be the "date in" found on the Express Mail label. If there is a discrepancy, you should submit a request for a corrected Filing Receipt along with a copy of the Express Mail label showing the "date in."

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